

Logical and, or, xor		Logical not
Source Operand	Target Operand	Target Operand
<reg>	<reg>	<reg>
<mem>	<reg>	<mem>
<reg>	<mem>	
<con>	<reg>	
<con>	<mem>	

Figure 26.5 The allowable combinations of operands for *and*, *or*, *xor*, and *not* instructions.