

<b>Device</b>	<b>Address Range</b>		
memory 1	0x000000	through	0xffff
memory 2	0x100000	through	0x1ffff
device 1	0x200000	through	0x20000b
device 2	0x20000c	through	0x200017

**Figure 15.12** One possible assignment of bus addresses for the set of devices shown in Figure 15.11.