ALU			OP$_1$	OP$_2$	RES$_1$	RES$_2$		REG. INTERFACE			
1	1	1	1	0	0	0	0	1	0	1	1	1

Figure 8.11 An example instruction that simultaneously continues an ALU operation and loads the value from register seven into operand hardware unit one. Horizontal microcode makes parallelism easy to specify.