

ALU			OP <sub>1</sub>	OP <sub>2</sub>	RES <sub>1</sub>	RES <sub>2</sub>	REG. INTERFACE						
1	1	1	0	0	0	0	0	0	0	0	0	0	0

**Figure 8.10** An instruction that can be inserted to add delay processing to wait for the ALU to complete an operation. Timing and delay are crucial aspects of horizontal microcode.