

add r4, r2, r3

(a)

operation	reg A	reg B	dst reg	offset
0 0 0 0 1	0 0 1 0	0 0 1 1	0 1 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

(b)

**Figure 6.3** (a) An example *add* instruction as it appears to a programmer, and (b) the instruction stored in memory.