$$
\begin{array}{r}
\mathbf{1\ 0\ 0} \\
\mathbf{+\ \ 1\ 1\ 0} \\
\hline
\mathbf{1\ 0\ 1\ 0}
\end{array}
$$

overflow   result

Figure 3.7 Illustration of addition with unsigned integers that produces overflow. The overflow indicator, which tells whether wraparound occurred, is equal to the carry bit.