Name	Bit Range	Purpose
N	31	Negative/less than
Z	30	Zero
C	29	Carry/borrow/extend
V	28	Overflow
Q	27	Sticky overflow
J	24	Java state
DNM	20 – 23	Do not modify
GE	16-19	Greater-than-or-equal-to
IT	10 – 15 and 25 – 26	The if-then state
E	9	Data endianness
A	8	Imprecise data abort disable
I	7	IRQ disable
F	6	FIQ disable
T	5	Thumb state
M	0 – 4	Processor mode

Figure 27.2 Bits in an ARM CPSR and the meaning of each.