Register	Name	Purpose
r15	pc	Program counter
r14	lr	Link register during function call
r13	sp	Stack pointer
r12	ip	Intra-procedural scratch
r11	fp	Frame or argument pointer
r10	sl	Stack limit
r9	v6	Local variable 6 (or real frame pointer)
r8	v5	Local variable 5
r7	v4	Local variable 4
r6	v3	Local variable 3
r5	v2	Local variable 2
r4	v1	Local variable 1
r3	a4	Argument 4 during a function call
r2	a3	Argument 3 during a function call
r1	a2	Argument 2 during a function call
r0	a1	Argument 1 during a function call

Figure 27.1 The general-purpose registers in an ARM architecture, the alternate name used in assembly language, and the meaning assigned to each register.