

### Valid references

```
mov  eax, [lab1]      ; move 4 bytes from label lab1 in memory to EAX
mov  [lab2], ebx      ; store 4 bytes from EBX to label lab2 in memory
and  eax, [esi-4]     ; and EAX with 4 bytes at address given by ESI - 4
not  [edi+8]          ; invert 32 bits at location given by EDI + 8
mov  [eax+2*ebx], 0   ; store zero in 4 bytes at address given by EAX+2*EBX
mov  cl, [esi+4*ebx]  ; move byte from ESI+4*EBX into register CL
```

### Invalid references

```
mov  eax, [esi-ebx]   ; cannot subtract two registers
mov  [eax+ebx+cl], 0  ; cannot specify more than two registers
```

**Figure 26.10** Examples of valid and invalid memory references using Intel notation.